SIEMENS

Data sheet

6ES7313-5BF03-0AB0



Spare part SIMATIC S7-300, CPU 313C, Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 3 high-speed counters (30 kHz), Integr. power supply 24 V DC, work memory 64 KB, Front connector (2x 40-pole) and Micro Memory Card required

Figure similar

F 90 11 10 10 10 10 10 10 10 10 10 10 10 10	
General information	
HW functional status	01
Firmware version	V2.6
Engineering with	
Programming package	STEP 7 V5.3 SP2 or higher with HW update
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Load voltage L+	
 Rated value (DC) 	24 V
 permissible range, lower limit (DC) 	20.4 V
 permissible range, upper limit (DC) 	28.8 V
Digital inputs	
— load voltage / at digital input / at DC / rated value	24 V
 Reverse polarity protection 	Yes
Digital outputs	
— Rated value (DC)	24 V
 Reverse polarity protection 	No
Analog outputs	
— load voltage / at analog output / at DC / rated value	24 V
Reverse polarity protection	Yes
Input current	
Current consumption (rated value)	700 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	11 A
I ² t	0.7 A ² ·s
Digital inputs	
• from load voltage L+ (without load), max.	70 mA
Digital outputs	
from load voltage L+, max.	100 mA
Power loss	
Power loss, typ.	14 W
Memory	
Work memory	
• integrated	64 kbyte
• expandable	No

Load memory	
• Plug-in (MMC)	Yes
Plug-in (MMC), max.	8 Mbyte
 Data management on MMC (after last programming), min. 	10 a
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.1 µs
for bit operations, max.	0.2 µs
for word operations, typ.	0.2 µs
for fixed point arithmetic, typ.	2 µs
for floating point arithmetic, typ.	3 µs
CPU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be
DB	reduced by the MMC used.
Number, max.	511; Number range: 1 to 511
• Size, max.	16 kbyte
FB	
Number, max.	1 024; Number range: 0 to 2047
• Size, max.	16 kbyte
FC	
Number, max.	1 024; Number range: 0 to 2047
• Size, max.	16 kbyte
OB	
• Size, max.	16 kbyte
 Number of free cycle OBs 	1; OB 1
 Number of time alarm OBs 	1; OB 10
 Number of delay alarm OBs 	1; OB 20
 Number of cyclic interrupt OBs 	1; OB 35
 Number of process alarm OBs 	1; OB 40
 Number of startup OBs 	1; OB 100
 Number of asynchronous error OBs 	4; OB 80, 82, 85, 87
Number of synchronous error OBs	2; OB 121, 122
Nesting depth	
 per priority class 	8
additional within an error OB	4
Counters, timers and their retentivity	
S7 counter	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	8
Counting range — lower limit	0
	999
— upper limit IEC counter	000
• present	Yes
• Type	SFB
Number	Unlimited (limited only by RAM capacity)
S7 times	Similar (ministration of the first output)
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	No retentivity

Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
Number	Unlimited (limited only by RAM capacity)
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	64 kbyte
Flag	
• Size, max.	256 byte
Retentivity available	Yes; MB 0 to MB 255
Retentivity preset	MB 0 to MB 15
Number of clock memories	8; 1 memory byte
Data blocks	
Retentivity adjustable	Yes; via non-retain property on DB
Retentivity preset	Yes
Local data	
per priority class, max.	510 byte
Address area	
I/O address area	
Inputs	1 kbyte
Outputs	1 kbyte
Process image	
Inputs	128 byte
Outputs	128 byte
Default addresses of the integrated channels	
— Digital inputs	124.0 to 126.7
— Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
— Analog outputs	752 to 755
Digital channels	
Inputs	1 016
— of which central	1 016
Outputs	1 008
— of which central	1 008
Analog channels	
Inputs	253
— of which central	253
Outputs	250
— of which central	250
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	
• integrated	none
• via CP	4
Number of operable FMs and CPs (recommended)	
• FM	8
• CP, PtP	8
• CP, LAN	6
Rack	
• Racks, max.	4
Modules per rack, max.	8; In rack 3 max. 7
Time of day	
Clock	
Hardware clock (real-time)	Yes
retentive and synchronizable	Yes
Backup time	6 wk; At 40 °C ambient temperature
Deviation per day, max.	10 s
Operating hours counter	

- Niveskov	4
Number / Number range	1
Number/Number range Denge of values	
Range of values	0 to 2 ³ 1 hours (when using SFC 101)
Granularity retentive	1 h
Clock synchronization	Yes; Must be restarted at each restart
• supported	Yes
• to MPI, master	Yes
• to MPI, slave	Yes
• in AS, master	Yes
Digital inputs	163
Number of digital inputs	24
of which inputs usable for technological functions	12
integrated channels (DI)	24
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	165
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
Rated value (DC)	24 V
• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30 V
Input current	
• for signal "1", typ.	9 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms
— Rated value	3 ms
for technological functions	
— at "0" to "1", max.	16 µs
Cable length	
shielded, max.	1 000 m; 100 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	
— shielded, max.	100 m
— unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	16
 of which high-speed outputs 	4
integrated channels (DO)	16
Short-circuit protection	Yes; Clocked electronically
Response threshold, typ.	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Limitation of inductive shutdown voltage to Controlling a digital input	L+ (-48 V) Yes
Controlling a digital input Switching capacity of the outputs on lamp load, max.	
Controlling a digital input Switching capacity of the outputs	Yes
Controlling a digital input Switching capacity of the outputs on lamp load, max.	Yes
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit	Yes 5 W
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage	Yes 5 W 48 Ω 4 kΩ
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min.	Yes 5 W 48 Ω
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current	Yes 5 W 48Ω $4 \text{ k}\Omega$ L+ (-0.8 V)
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current for signal "1" rated value	Yes 5 W 48Ω $4 \text{ k}\Omega$ L+ (-0.8 V) 500 mA
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current for signal "1" rated value for signal "1" permissible range, min.	Yes 5 W 48 Ω 4 kΩ L+ (-0.8 V) 500 mA 5 mA
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current for signal "1" rated value for signal "1" permissible range, min. for signal "1" permissible range, max.	Yes 5 W 48 Ω 4 kΩ L+ (-0.8 V) 500 mA 5 mA 0.6 A
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current for signal "1" rated value for signal "1" permissible range, min. for signal "1" permissible range, max. for signal "1" minimum load current	Yes 5 W 48 Ω 4 kΩ L+ (-0.8 V) 500 mA 5 mA 0.6 A 5 mA
Controlling a digital input Switching capacity of the outputs on lamp load, max. Load resistance range lower limit upper limit Output voltage for signal "1", min. Output current for signal "1" rated value for signal "1" permissible range, min. for signal "1" permissible range, max.	Yes 5 W 48 Ω 4 kΩ L+ (-0.8 V) 500 mA 5 mA 0.6 A

e for uprating	No
 for uprating for redundant control of a load 	No Yes
	res
Switching frequency	400 11
with resistive load, max.	100 Hz
with inductive load, max.	0.5 Hz
• on lamp load, max.	100 Hz
of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	
 For voltage/current measurement 	4
For resistance/resistance thermometer measurement	1
integrated channels (AI)	4+1
permissible input voltage for current input (destruction limit), max.	5 V; Permanent
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Electrical input frequency, max.	400 Hz
No-load voltage for resistance-type transmitter, typ.	2.5 V
Constant measurement current for resistance-type transmitter, typ.	1.8 to 3.3 mA
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
Current	Yes
Resistance thermometer	Yes; Pt 100 / 10 MΩ
Resistance	Yes
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents	
• 0 to 20 mA	Yes
— Input resistance (0 to 20 mA)	100 Ω
• -20 mA to +20 mA	Yes
— Input resistance (-20 mA to +20 mA)	100 Ω
• 4 mA to 20 mA	Yes
— Input resistance (4 mA to 20 mA)	100 Ω
Input ranges (rated values), resistance thermometer	
• Pt 100	Yes
— Input resistance (Pt 100)	10 ΜΩ
Input ranges (rated values), resistors	
• 0 to 600 ohms	Yes
— Input resistance (0 to 600 ohms)	10 ΜΩ
Thermocouple (TC)	
Temperature compensation	
	No
parameterizable Characteristic linearization	INU
	Vec. by software
parameterizable for registance thermometer.	Yes; by software
— for resistance thermometer	Pt 100
Cable length	100 m
 shielded, max. 	

Analog outputs Number of analog outputs	2
Number of analog outputs	
integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	17 V
Output ranges, voltage	
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	
 for voltage output two-wire connection 	Yes; Without compensation of the line resistances
 for voltage output four-wire connection 	No
for current output two-wire connection	Yes
Load impedance (in rated range of output)	
 with voltage outputs, min. 	1 kΩ
 with voltage outputs, capacitive load, max. 	0.1 μF
with current outputs, max.	300 Ω
 with current outputs, inductive load, max. 	0.1 mH
Destruction limits against externally applied voltages and currents	
 voltage / between the analog outputs and reference potential of the analog measuring circuit / as destruction limit for externally applied voltage / maximum permissible 	16 V; Permanent
current / at the analog outputs / as destruction limit for externally applied voltage / maximum permissible	50 mA; Permanent
Cable length	
• shielded, max.	200 m
Analog value generation for the inputs	
Measurement principle	Actual value encryption (successive approximation)
Integration and conversion time/resolution per channel	
 Resolution with overrange (bit including sign), max. 	12 bit
Integration time, parameterizable	Yes; 2,5 / 16,6 / 20 ms
Interference voltage suppression for interference frequency f1 in Hz	400 / 60 / 50 Hz
 Time constant of the input filter 	0.38 ms
 Basic execution time of the module (all channels 	1 ms
released)	
Analog value generation for the outputs	
Analog value generation for the outputs	12 bit
Analog value generation for the outputs Integration and conversion time/resolution per channel	
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max.	12 bit
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel)	12 bit
Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time	12 bit 1 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load	12 bit 1 ms 0.6 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for capacitive load	12 bit 1 ms 0.6 ms 1 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel • Resolution with overrange (bit including sign), max. • Conversion time (per channel) Settling time • for resistive load • for capacitive load • for inductive load	12 bit 1 ms 0.6 ms 1 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder	12 bit 1 ms 0.6 ms 1 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders	12 bit 1 ms 0.6 ms 1 ms 0.5 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for voltage measurement	12 bit 1 ms 0.6 ms 1 ms 0.5 ms
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Fincoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection connectable encoders	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection Connectable encoders 2-wire sensor	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No Yes
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Fincoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection Connectable encoders 2-wire sensor permissible quiescent current (2-wire sensor), max.	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection Connectable encoders 2-wire sensor permissible quiescent current (2-wire sensor), max. Errors/accuracies	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No Yes 1.5 mA
Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Fincoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection Connectable encoders 2-wire sensor permissible quiescent current (2-wire sensor), max.	12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No Yes

Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)	0.06 %
Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-)	0.1 %
Linearity error (relative to output range), (+/-)	0.15 %
Temperature error (relative to output range), (+/-)	0.01 %/K
Crosstalk between the outputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to output range), (+/-)	0.06 %
Operational error limit in overall temperature range	
Voltage, relative to input range, (+/-)	1 %
Current, relative to input range, (+/-)	1 %
• Resistance, relative to input range, (+/-)	5 %
Voltage, relative to output range, (+/-)	1 %
Current, relative to output range, (+/-)	1 %
Basic error limit (operational limit at 25 °C)	
Voltage, relative to input range, (+/-)	0.7 %; Linearity error ±0.06 %
• Current, relative to input range, (+/-)	0.7 %; Linearity error ±0.06 %
• Resistance, relative to input range, (+/-)	3 %; Linearity error ±0.2 %
Resistance thermometer, relative to input range, (+/-)	3 %
 Voltage, relative to output range, (+/-) 	0.7 %
Current, relative to output range, (+/-)	0.7 %
Interference voltage suppression for $f = n \times (f1 + /- 1 \%)$, $f1 = interf$	
Series mode interference (peak value of interference < rated value of input range), min.	30 dB
Common mode interference, min.	40 dB
Interfaces	
Number of industrial Ethernet interfaces	0
Number of PROFINET interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	0
MPI	
Cable length, max.	50 m; without repeater
1. Interface	
Interface type	Integrated RS 485 interface
Isolated	No
Interface types	
• RS 485	Yes
Output current of the interface, max.	200 mA
Protocols	
• MPI	Yes
PROFIBUS DP master	No
 PROFIBUS DP slave 	No
Point-to-point connection	No
MPI	
 Number of connections 	8
Transmission rate, max.	187.5 kbit/s
Services	
— PG/OP communication	Yes
— Routing	No
 Global data communication 	Yes
 S7 basic communication 	Yes
— S7 communication	Yes
 S7 communication, as client 	No
— S7 communication, as server	Yes
Protocols	
PROFIsafe	No
communication functions / header	
PG/OP communication	Yes
Global data communication	
• supported	Yes
 Number of GD loops, max. 	4

 Number of GD packets, max. 	4
 Number of GD packets, transmitter, max. 	4
 Number of GD packets, receiver, max. 	4
 Size of GD packets, max. 	22 byte
 Size of GD packet (of which consistent), max. 	22 byte
S7 basic communication	
 communication function / S7 basic communication 	Yes
 User data per job, max. 	76 byte
 User data per job (of which consistent), max. 	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET
	as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
 User data per job, max. 	180 byte; With PUT/GET
 User data per job (of which consistent), max. 	64 byte
S5 compatible communication	
supported	Yes; via CP and loadable FC
Number of connections	
• overall	8
 usable for PG communication 	7
 reserved for PG communication 	1
— adjustable for PG communication, min.	1
adjustable for PG communication, max.	7
usable for OP communication	7
 reserved for OP communication 	1
 adjustable for OP communication, min. 	1
adjustable for OP communication, max.	7
usable for S7 basic communication	4
reserved for S7 basic communication	0
adjustable for S7 basic communication, min.	0
adjustable for S7 basic communication, max.	4
usable for routing	No
S7 message functions	INU
	9: Depending on the configured connections for DC/QD and C7 hadis
Number of login stations for message functions, max.	8; Depending on the configured connections for PG/OP and S7 basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	20
Test commissioning functions	
Status block	Yes
Single step	Yes
Number of breakpoints	2
Status/control	
Status/control variable	Yes
Status/control variable Variables	
	Inputs, outputs, memory bits, DB, times, counters
Number of variables, max.	30
— of which status variables, max.	30
— of which control variables, max.	14
Forcing	V
• Forcing	Yes
Forcing, variables	Inputs, outputs
Number of variables, max.	10
Diagnostic buffer	
• present	Yes
Number of entries, max.	100
Interrupts/diagnostics/status information	
Diagnostics indication LED	
 Status indicator digital input (green) 	Yes
 Status indicator digital output (green) 	Yes
Integrated Functions	
Frequency measurement	Yes

controlled positioning No Ves; PID controller (see "Technological Functions" manual) Integrated function blocks (closed-loop control) Ves (PID controller) Ves Number of pulse outputs S, 3: channels pulse width modulation up to max. 2.5 kHz (see "Technological Functions" manual) Limit frequency (pulse) Potential separation digital inputs Potential separation digital inputs Potential separation digital inputs Potential separation digital inputs Potential separation digital outputs Potential separation and backplane bus Potential separation analog inputs Potential separation analog outputs Potential sep	 Number of frequency meters 	3; 3 channels up to max. 30 kHz (see "Technological Functions" manual)
integrated function blocks (closed-loop control) PID controller Yes 3, 3 channels pulse width modulation up to max. 2.5 kHz (see "Technological Functions" manual) Limit frequency (pulse) 2,5 kHz Limit frequency (pulse) Potential separation Potential separation digital inputs • Potential separation digital inputs • Potential separation digital toutputs • Potential separation digital outputs • Potential separation and backplane bus Potential separation analog inputs • Detween the channels in groups of set where the channels and backplane bus • Potential separation analog inputs • Potential separation analog puts • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Potential separation analog outputs • Potential separation analog outputs		
PID controller Number of pulse outputs 3. 3. channels pulse width modulation up to max. 2.5 kHz (see "Technological Functions" manual) Limit frequency (pulse) 2.5 kHz Potential separation digital inputs • Potential separation digital inputs • between the channels and backplane bus • Potential separation digital outputs • between the channels in groups of 8 • between the channels in groups of 8 • between the channels and backplane bus Potential separation analog inputs • Potential separation analog outputs • between the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus Potential separation analog outputs • Detween the channels and backplane bus • System function floots (SFC) • System function floots (SFC) • System function blocks (SFB) • System function blocks (SFB)		Yes: PID controller (see "Technological Functions" manual)
Number of pulse outputs Secretary (pulse 2.5 kHz (see "Technological Functions" manual)		
Potential separation digital inputs Potential separation digital inputs Potential separation digital inputs Potential separation digital outputs Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs Potential separation analog outputs Potentia		3; 3 channels pulse width modulation up to max. 2.5 kHz (see "Technological
Potential separation digital inputs Potential separation digital inputs Potential separation digital inputs Potential separation digital outputs Potential separation analog inputs Potential separation analog outputs Potential separation analog inclick Potential separation analog inc	Limit frequency (pulse)	2.5 kHz
Potential separation digital inputs between the channels and backplane bus Potential separation digital outputs Potential separation and backplane bus Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs Potential separation analog outputs Potential separatio	Potential separation	
between the channels and backplane bus Yes Potential separation digital outputs Potential separation digital outputs between the channels yes between the channels in groups of between the channels and backplane bus Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs Potential separation analog inputs between the channels No Bolation Solation tested with 600 V DC configuration / header Configuration / header Configuration / programming / header Command set No Solation (SFC) See instruction list No System functions (SFC) See instruction list No System functions (SFC) System functions (SF	Potential separation digital inputs	
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System functions (SFC) System function blocks (SFB) Programming language	Isolation tested with configuration / header Configuration software • STEP 7 configuration / programming / header	Yes; V5.3 SP2 with HW update
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Programming language — LAD — FBD — FBD — STL — SCL — GRAPH — HiGraph® Know-how protection • User program protection/password protection Pimensions Width 120 mm Height Depth 130 mm	Isolation tested with configuration / header Configuration software • STEP 7 configuration / programming / header • Command set • Nesting levels	Yes; V5.3 SP2 with HW update see instruction list 8
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— STL Yes — SCL Yes — GRAPH Yes — HiGraph® Yes Know-how protection ✓ User program protection/password protection Yes Dimensions Width 120 mm Height 125 mm Depth 130 mm	Isolation tested with configuration / header Configuration software • STEP 7 configuration / programming / header • Command set • Nesting levels • System functions (SFC) • System function blocks (SFB) Programming language	Yes; V5.3 SP2 with HW update see instruction list 8 see instruction list see instruction list
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Weight, approx. 660 g	Isolation tested with configuration / header Configuration software STEP 7 configuration / programming / header Command set Nesting levels System functions (SFC) System function blocks (SFB) Programming language LAD FBD STL SCL GRAPH HiGraph® Know-how protection User program protection/password protection Dimensions Width Height Depth	Yes; V5.3 SP2 with HW update see instruction list 8 see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Ye

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